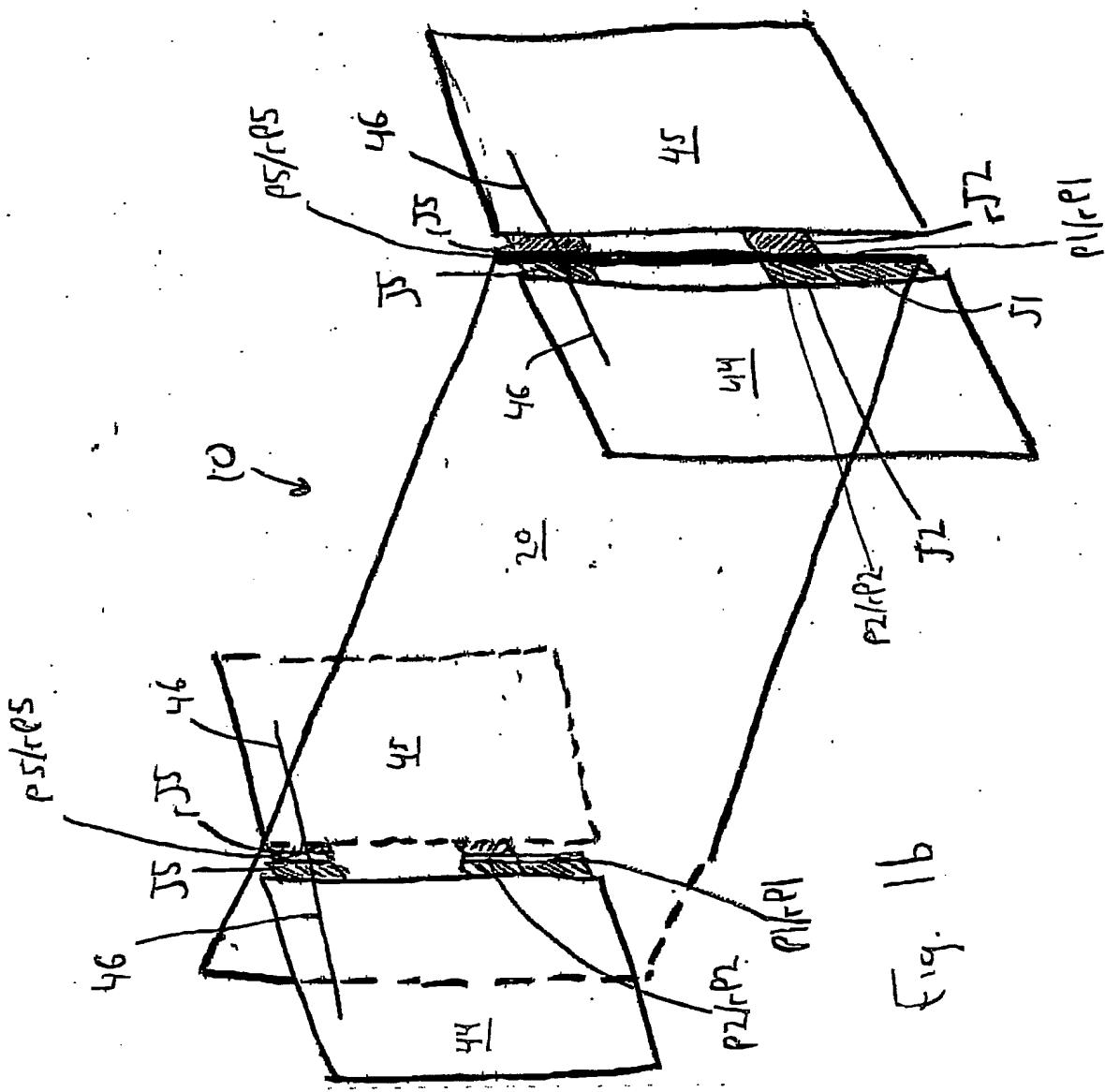


Fig. 1a

U.S. PATENT & TRADEMARK OFFICE  
MAR 22 2004



MAR 22 2004

ENT. & TRAC.

REASER

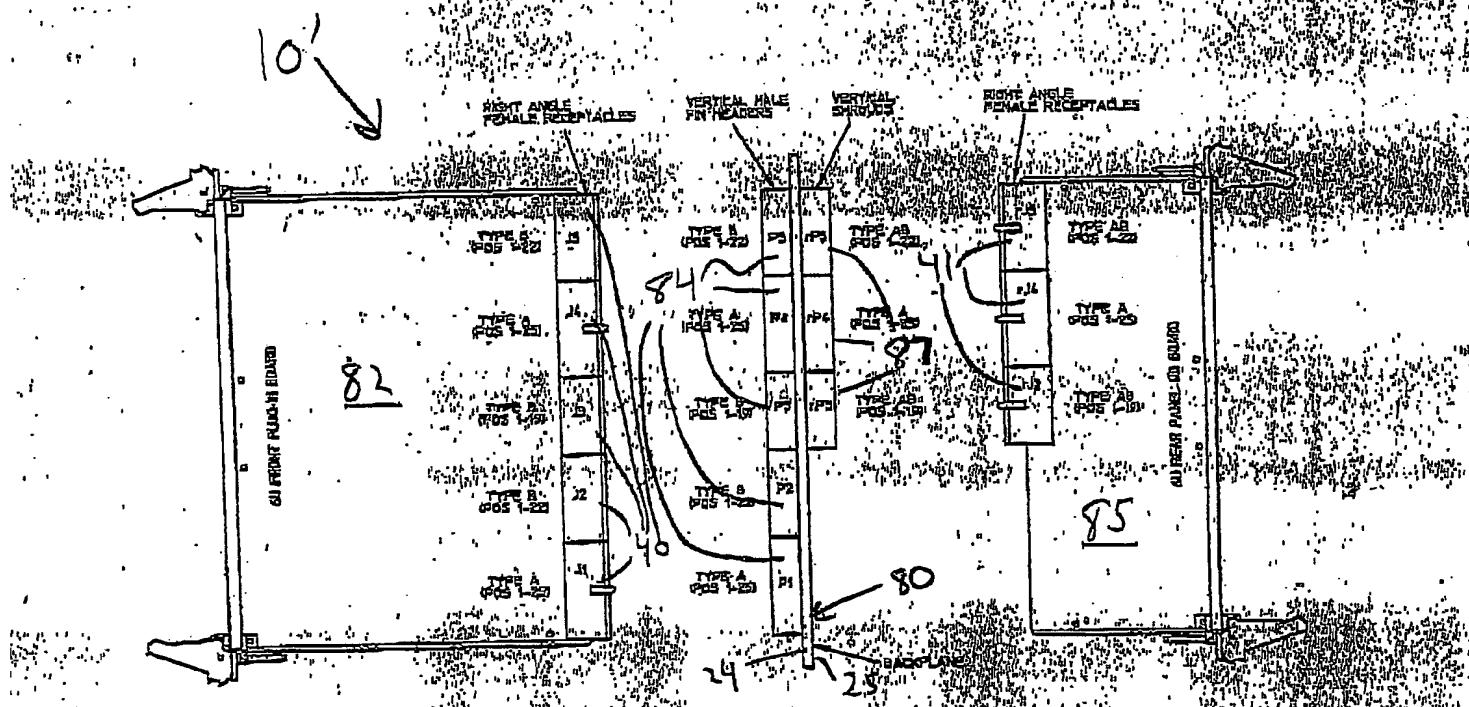


Fig. 1. 2a

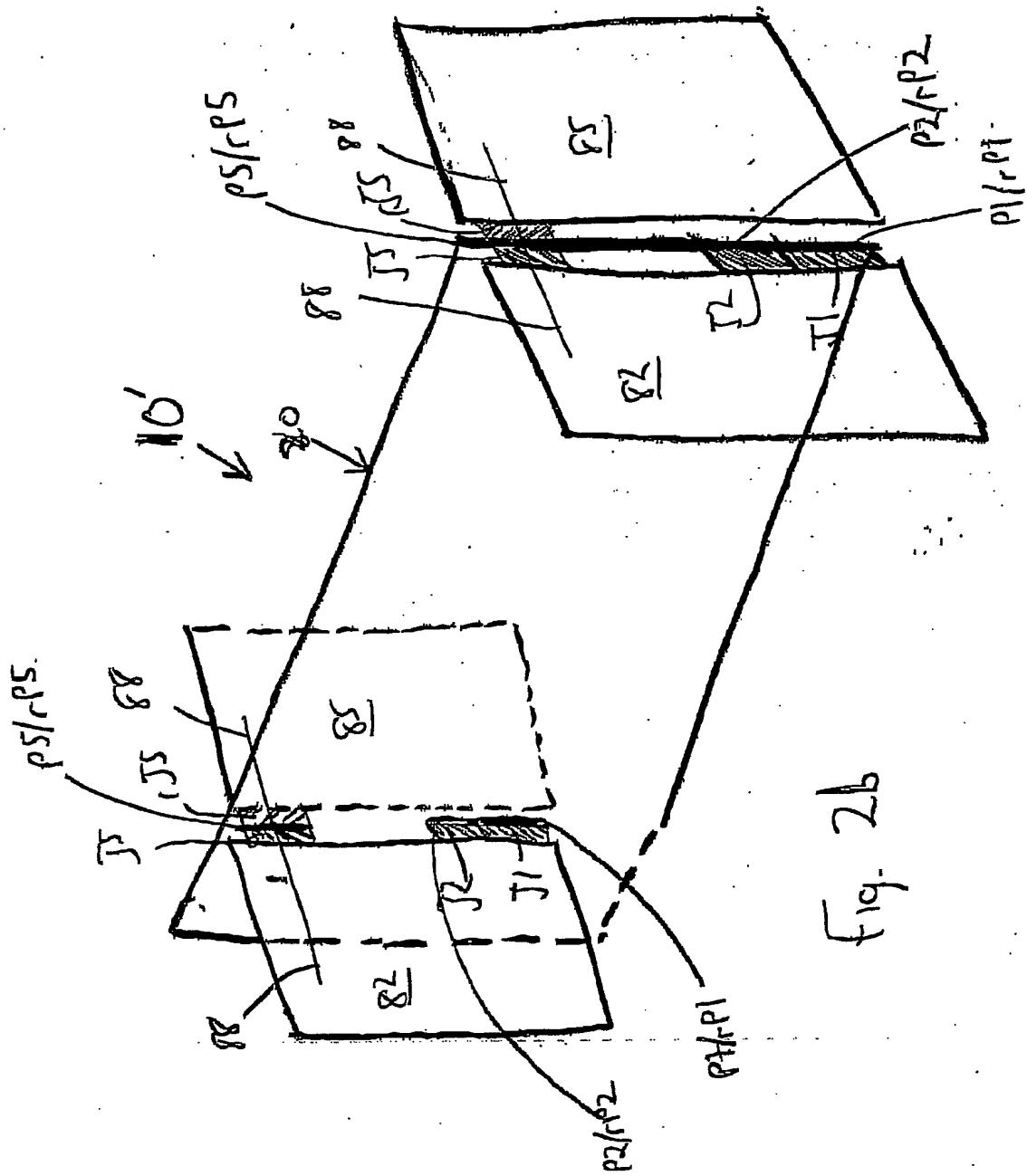
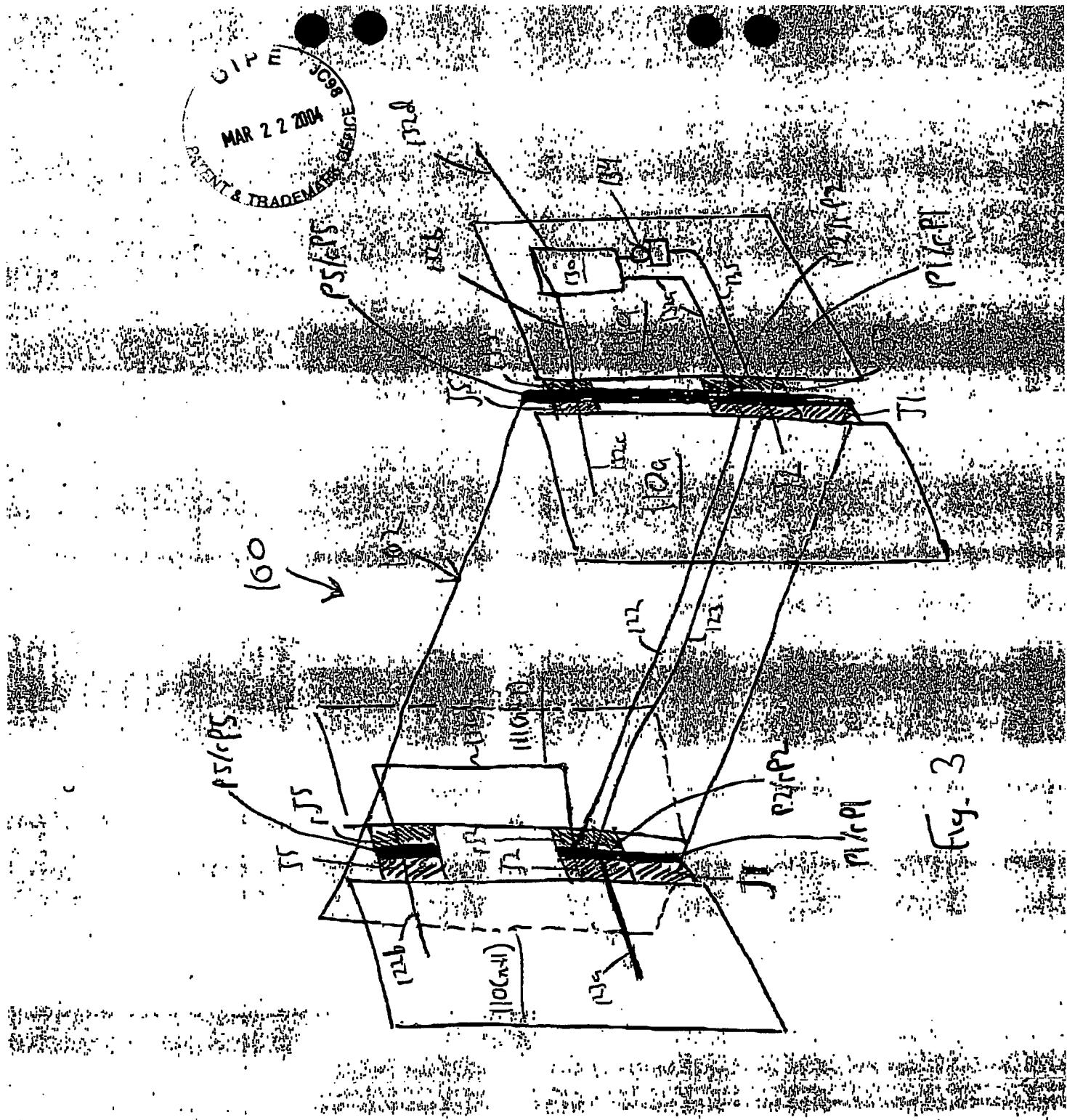


Fig. 2b



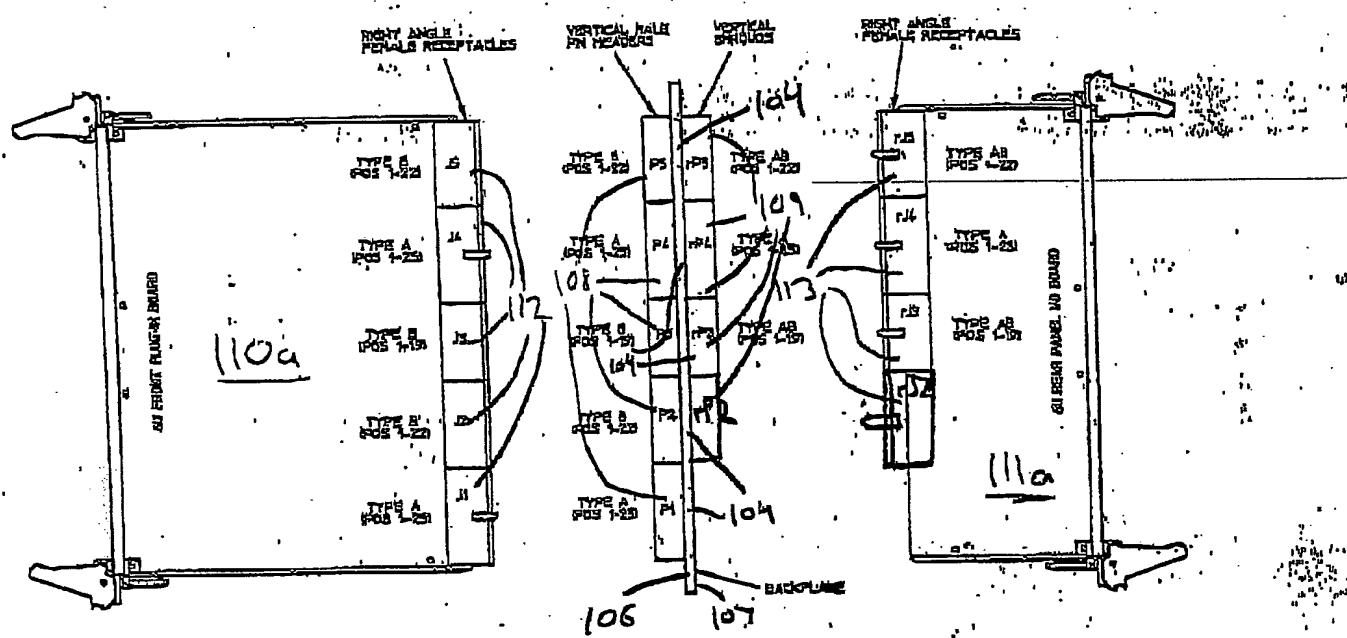
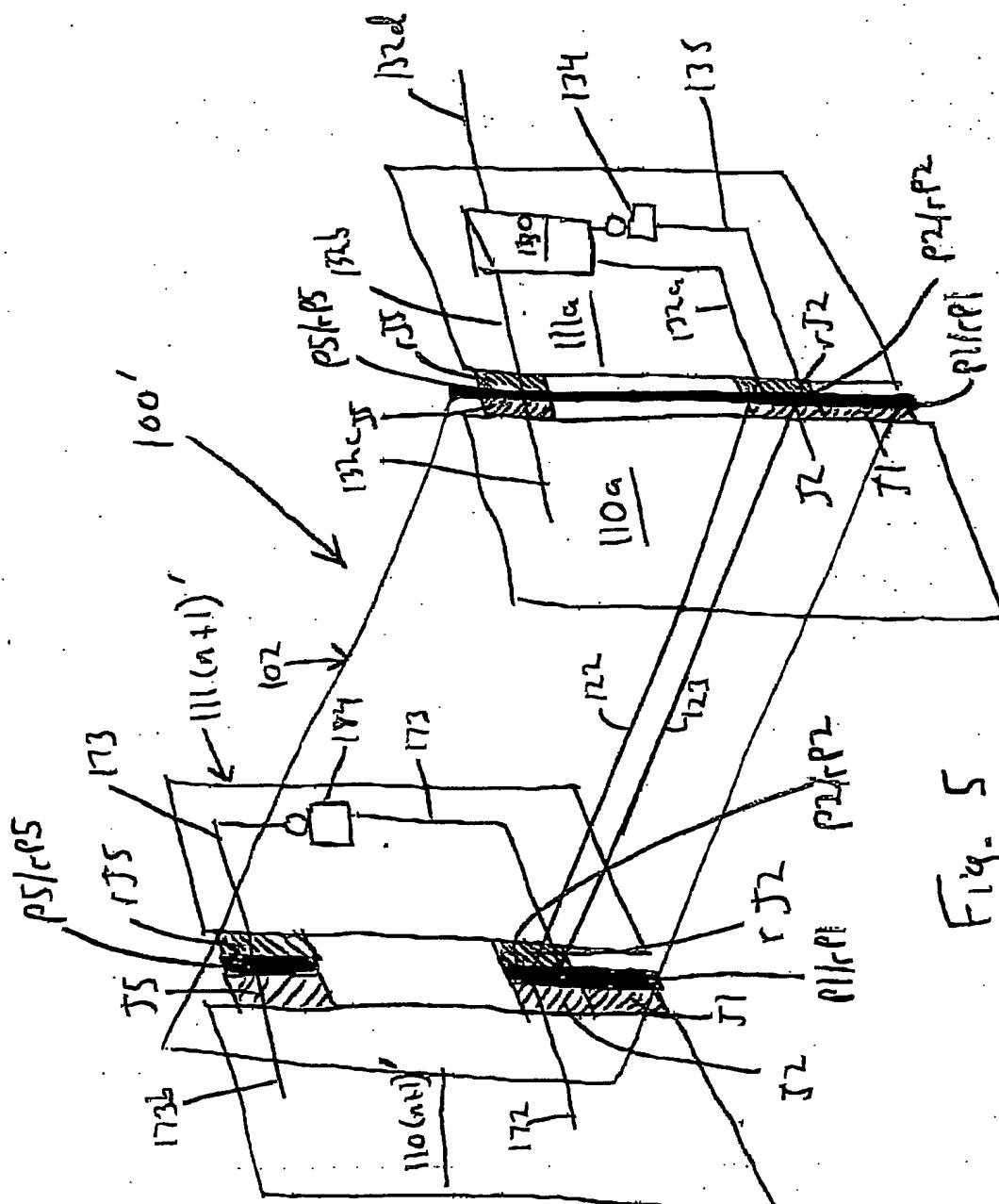
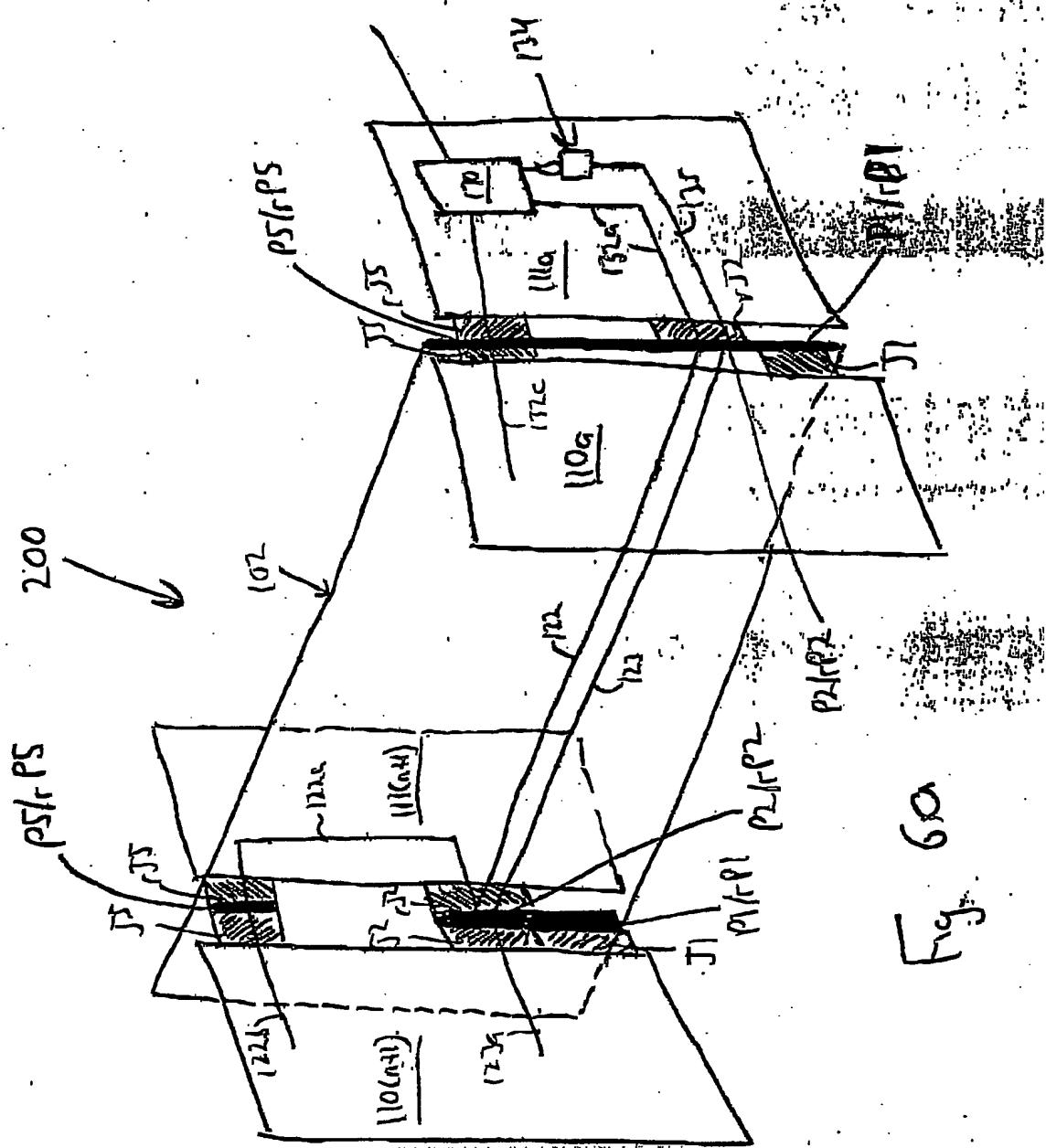
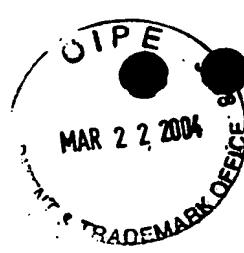
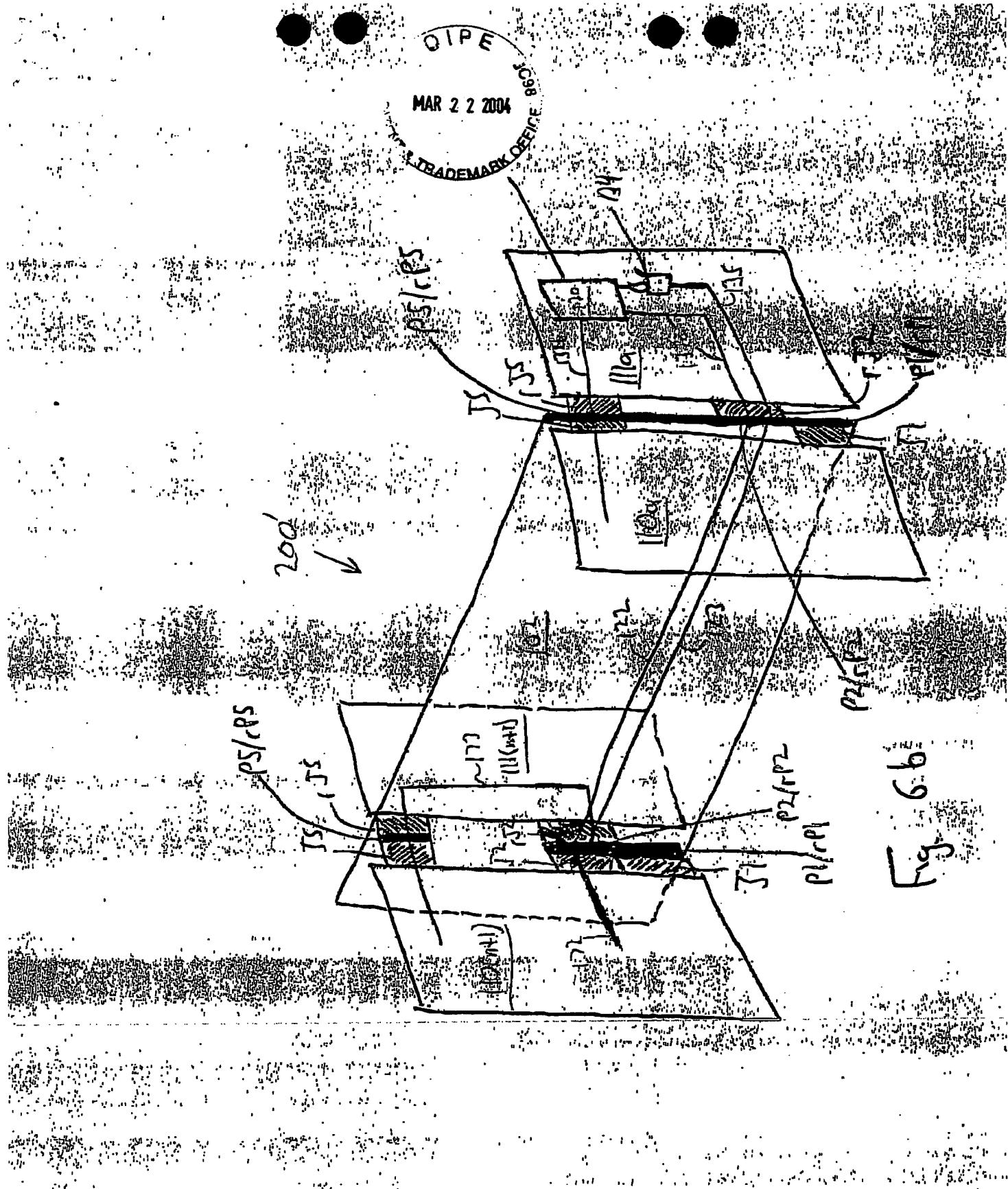


Fig. 4



۵





四  
六

# TABLE 1

## Pin Assignments

Table 13. CompactPCI Peripheral Slot 64-Bit Connector Pin Assignments<sup>(1)(10,11)</sup>

Pin	GND	GA4 <sup>(12)</sup>	GA3 <sup>(13)</sup>	GA2 <sup>(12)</sup>	GA1 <sup>(12)</sup>	GA0 <sup>(12)</sup>	GND	P2 / J2
22	GND	RSV	RSV	RSV	RSV	RSV	GND	CONNECTOR
21	GND	RSV	RSV	RSV	GND	RSV	GND	
20	GND	RSV	RSV	RSV	RSV	RSV	GND	
19	GND	RSV	RSV	RSV	RSV	RSV	GND	
18	GND	BR5VP2A18	BR5VP2B18	BR5VP2C18	GND	BR5VP2E18	GND	
17	GND	BR5VP2A17	GND	RSV	RSV	RSV	GND	
16	GND	BR5VP2A18	BR5VP2B18	RSV	GND	BR5VP2E16	GND	
15	GND	BR5VP2A15	GND	RSV	RSV	RSV	GND	
14	GND	AD[25]	AD[34]	AD[33]	GND	AD[32]	GND	
13	GND	AD[38]	GND	V[10] <sup>(12)</sup>	AD[37]	AD[38]	GND	
12	GND	AD[42]	AD[41]	AD[40]	GND	AD[39]	GND	
11	GND	AD[45]	GND	V[10] <sup>(12)</sup>	AD[44]	AD[45]	GND	
10	GND	AD[49]	AD[48]	AD[47]	GND	AD[48]	GND	
9	GND	AD[52]	GND	V[10] <sup>(12)</sup>	AD[51]	AD[50]	GND	
8	GND	AD[56]	AD[55]	AD[54]	GND	AD[53]	GND	
7	GND	AD[59]	GND	V[10] <sup>(12)</sup>	AD[58]	AD[57]	GND	
6	GND	AD[63]	AD[62]	AD[61]	GND	AD[60]	GND	
5	GND	C/BE[8] <sup>#</sup>	GND	V[10] <sup>(12)</sup>	C/BE[4] <sup>#</sup>	PAR	GND	
4	GND	V[10] <sup>(12)</sup>	BR5VP2E4	C/BEV#	GND	C/BE[5] <sup>#</sup>	GND	
3A	GND	RSV	GND	RSV	RSV	RSV	GND	
2P	GND	RSV	RSV	UNC <sup>(12)</sup>	RSV	RSV	GND	
1A	GND	RSV	GND	RSV	RSV	RSV	GND	
25	GND	5V	REQ64#	ENUM#	3.3V	5V	GND	
24	GND	AD[11]	5V	AD[12]	AD[11]	AD[64#]	GND	
23	GND	3.3V	AD[41]	AD[31]	AD[31]	AD[2]	GND	
22	GND	AD[71]	GND	AD[61]	AD[61]	AD[5]	GND	
21	GND	3.3V	AD[91]	AD[81]	AD[81]	C/BE[10] <sup>#</sup>	GND	
20	GND	AD[121]	GND	V[10] <sup>(12)</sup>	AD[111]	AD[10]	GND	
19	GND	3.3V	AD[151]	AD[141]	AD[141]	AD[131]	GND	
18	GND	SERR#	GND	3.3V	PAR	C/BE[11] <sup>#</sup>	GND	
17	GND	3.3V	IPMB SCL	IPMB SDA	IPMB SDA	SERR#	GND	
16	GND	RESEL#	GND	V[10] <sup>(12)</sup>	STOP#	LOCK#	GND	
15	GND	3.3V	FRAME#	IRDY#	HD SEL#	TRDY#	GND	
12-14				KEY AREA				
11	GND	AD[18]	AD[17]	AD[15]	AD[14]	C/BE[21]	GND	
10	GND	AD[21]	GND	3.3V	AD[20]	AD[18]	GND	
9	GND	C/BE[31] <sup>#</sup>	IDSEL#	AD[31]	AD[31]	AD[28]	GND	
8	GND	AD[28]	GND	V[10] <sup>(12)</sup>	AD[28]	AD[24]	GND	
7	GND	AD[30]	AD[29]	AD[28]	AD[28]	AD[27]	GND	
6	GND	REQ#	GND	AD[28]	CLK	AD[21]	GND	
5	GND	BR5VP1A5	BR5VP1B5	RST#	AD[28]	GND#	GND	
4	GND	IPMB PWR	HEALTHY# <sup>(12)</sup>	AD[28]	INTP	INTS	GND	
3	GND	INTA#	INTB#	INTC#	AD[28]	INTD#	GND	
2	GND	TCK <sup>(12)</sup>	5V	TMS#	TDO <sup>(12)</sup>	TDI <sup>(12)</sup>	GND	
1	GND	5V	-12V	TRST# <sup>(12)</sup>	+12V	5V	GND	
Pin	2 <sup>(12)</sup>	A	B	C	D	E	F <sup>(12)</sup>	

= long pins (front only)

= short pins (front only)

= medium length pins (front only)

PICMG 2.0 R3.0 10/1/99

Fig. 7